



<b>Title of Change:</b>	Final PCN for assembly and final test site addition SPEL Semiconductor Limited.																																																
<b>Proposed first ship date:</b>	20 April 2016																																																
<b>Contact information:</b>	Contact your local ON Semiconductor Sales Office or <Yasuhiro.Igarashi@onsemi.com>																																																
<b>Samples:</b>	Contact your local ON Semiconductor Sales Office																																																
<b>Additional Reliability Data:</b>	Contact your local ON Semiconductor Sales Office or <Kazutoshi.Kitazume@onsemi.com>.																																																
<b>Type of notification:</b>	This is a Final Product/Process Change Notification (FPCN) sent to customers. FPCNs are issued 90 days prior to implementation of the change. ON Semiconductor will consider this change accepted, unless an inquiry is made in writing within 30 days of delivery of this notice. To do so, contact <PCN.Support@onsemi.com>.																																																
<b>Change Part Identification:</b>	Affected products will be identified with the representation of month date code.																																																
		ON Seremban	SPEL semiconductor																																														
	Month date code	M	IM																																														
<b>Change category:</b>	<input type="checkbox"/> Wafer Fab Change <input checked="" type="checkbox"/> Assembly Change <input checked="" type="checkbox"/> Test Change <input type="checkbox"/> Other _____																																																
<b>Change Sub-Category(s):</b>	<input checked="" type="checkbox"/> Manufacturing Site Change/Addition <input type="checkbox"/> Material Change <input type="checkbox"/> Datasheet/Product Doc change <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Product specific change <input type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Other: _____																																																
<b>Sites Affected:</b>	<input type="checkbox"/> All site(s) <input type="checkbox"/> not applicable <input type="checkbox"/> ON Semiconductor site(s) : <input checked="" type="checkbox"/> External Foundry/Subcon site(s) SPEL Semiconductor Limited																																																
<b>Description and Purpose:</b>	This is a Final Process Change Notification to announce the addition of SPEL Semiconductor Limited as an assembly and final test site for the impacted device. The currently qualified assembly and final test site of ON Semiconductor Seremban, Malaysia will also continue to be used for this device. SPEL Semiconductor Limited is located in Chennai, India, and has obtained ISO9001 certification.																																																
<b>Reliability Data Summary:</b>	<table border="1"> <thead> <tr> <th>Test</th> <th>Specification</th> <th>Condition</th> <th>Interval</th> <th>Results</th> </tr> </thead> <tbody> <tr> <td>HTRB</td> <td>JESD22-A108</td> <td>Ta=150°C, 100%, VDSS=30V</td> <td>1008 hrs</td> <td>0/77*3lots</td> </tr> <tr> <td>HTSL</td> <td>JESD22-A103</td> <td>Ta=150°C</td> <td>1008 hrs</td> <td>0/77*3lots</td> </tr> <tr> <td>TC</td> <td>JESD22-A104</td> <td>Ta= -55°C to +150°C</td> <td>1000 cyc</td> <td>0/77*3lots</td> </tr> <tr> <td>H<sup>3</sup>TRB</td> <td>JESD22-A110</td> <td>85°C, 85% RH, 80%, VDSS=30V</td> <td>1008 hrs</td> <td>0/77*3lots</td> </tr> <tr> <td>AC</td> <td>JESD22-A118</td> <td>121°C, 100% RH, 2.03×10<sup>5</sup>Pa, unbiased</td> <td>96 hrs</td> <td>0/77*3lots</td> </tr> <tr> <td>PC</td> <td>J-STD-020 JESD-A113</td> <td>MSL 1 @ 260°C</td> <td></td> <td></td> </tr> <tr> <td>RSH</td> <td>JESD22- B106</td> <td>Ta = 265C, 10 sec</td> <td></td> <td>0/30*3lots</td> </tr> <tr> <td>SD</td> <td>JSTD002</td> <td>Ta = 245C, 10 sec</td> <td></td> <td>0/30*3lots</td> </tr> </tbody> </table>				Test	Specification	Condition	Interval	Results	HTRB	JESD22-A108	Ta=150°C, 100%, VDSS=30V	1008 hrs	0/77*3lots	HTSL	JESD22-A103	Ta=150°C	1008 hrs	0/77*3lots	TC	JESD22-A104	Ta= -55°C to +150°C	1000 cyc	0/77*3lots	H <sup>3</sup> TRB	JESD22-A110	85°C, 85% RH, 80%, VDSS=30V	1008 hrs	0/77*3lots	AC	JESD22-A118	121°C, 100% RH, 2.03×10 <sup>5</sup> Pa, unbiased	96 hrs	0/77*3lots	PC	J-STD-020 JESD-A113	MSL 1 @ 260°C			RSH	JESD22- B106	Ta = 265C, 10 sec		0/30*3lots	SD	JSTD002	Ta = 245C, 10 sec		0/30*3lots
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**Electrical Characteristic Summary:**

There is no change in the electrical performance. Datasheet specifications remain unchanged.

**List of Affected Standard Parts:**

Part Number	Qualification Vehicle
TF412ST5G	TF412T5G