COMPLIANT



Vishay Semiconductors

HEXFRED® Ultrafast Soft Recovery Diode, 140 A



PRIMARY CHARACTERISTICS					
V_{R}	600 V				
V _F (typical)	1.33 V				
t _{rr} (typical)	43 ns				
I _{F(DC)} at T _C , per module	140 A at 110 °C				
I _{F(AV)} at T _C , per module	140 A at 96 °C				
Package	SOT-227				

FEATURES

- · Fast recovery time characteristic
- · Electrically isolated base plate
- Large creepage distance between terminal
- · Simplified mechanical designs, rapid assembly
- Designed and qualified for industrial level
- UL approved file E78996
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The dual diode series configuration VS-HFA140FA60 is used for output rectification or freewheeling/clamping operation and high voltage application.

The semiconductor in the SOT-227 package is isolated from the copper base plate, allowing for common heatsinks and compact assemblies to be built.

These modules are intended for general applications such as power supplies, battery chargers electronic welders, motor control and inverters.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current per leg	l _F	T 110 °C	70	А	
per module		T _C = 110 °C	140		
Single pulse forward current	I _{FSM}	T _J = 25 °C	600		
Maximum navor discination nor los	P_{D}	T _C = 25 °C	357	W	
Maximum power dissipation, per leg		T _C = 110 °C	114	VV	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	600	-	-	
Forward voltage, per leg	V _{FM}	I _F = 60 A	-	1.33	1.70	V
		I _F = 120 A	-	1.56	2.04	
		I _F = 60 A, T _J = 125 °C	-	1.24	-	
		I _F = 60 A, T _J = 150 °C	-	1.19	-	
Reverse leakage current, per leg	I _{RM}	V _R = V _R rated	-	2.5	20	μΑ
		T _J = 125 °C, V _R = V _R rated	-	0.8	2	mA
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	3	9	IIIA



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 A; dI_F/dt = 20$	00 A/μs; V _R = 30 V	-	43	-	
Reverse recovery time, per leg t _{rr}	t _{rr}	T _J = 25 °C		-	90	-	ns
		T _J = 125 °C	$I_F = 50 \text{ A}$ $dI_F/dt = -200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	150	-	
Deale received a surrent received	_	T _J = 25 °C		-	9.5	-	Α
Peak recovery current, per leg	IRRM	T _J = 125 °C		-	17	-	Α
Reverse recovery charge, per leg	Q _{rr}	T _J = 25 °C		-	400	-	nC
		T _J = 125 °C		-	1180	-	110
Junction capacitance, per leg	C _T	V _R = 600 V		-	67	-	pF

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Б		-	-	0.35	
Junction to case, both legs conducting	R _{thJC}		-	-	0.175	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style				SC	T-227	

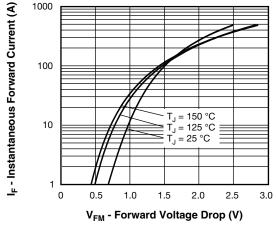


Fig. 1 - Typical Forward Voltage Drop Characteristics (Per Leg)

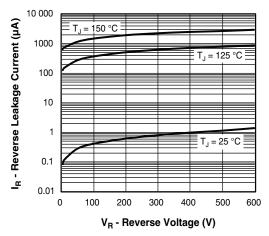


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

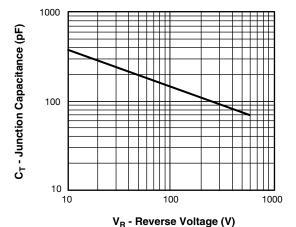


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

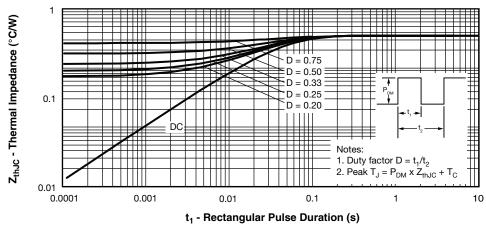


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

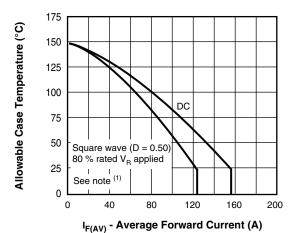


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

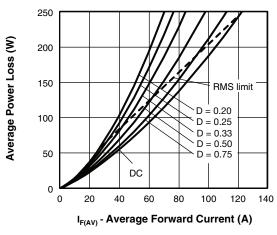


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

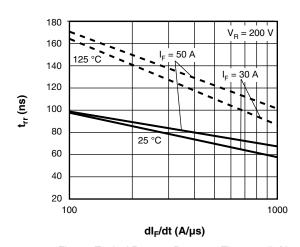


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

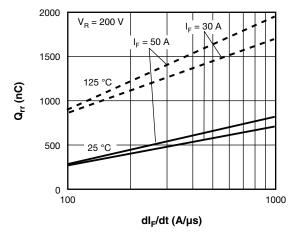


Fig. 8 - Typical Stored Charge vs. dI_F/dt

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 5); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = rated V_R$

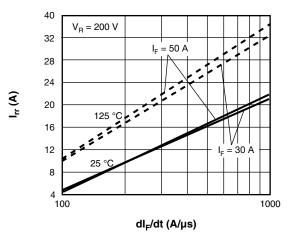


Fig. 9 - Typical Peak Recovery Current vs. dl_F/dt

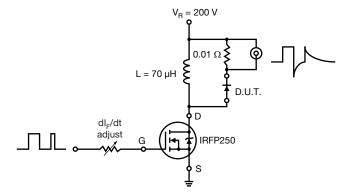
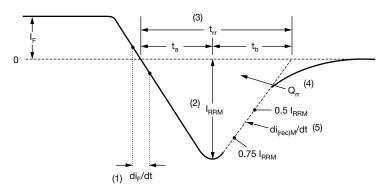


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

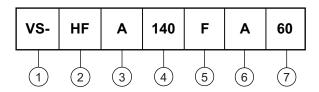
(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions



ORDERING INFORMATION TABLE

Device code



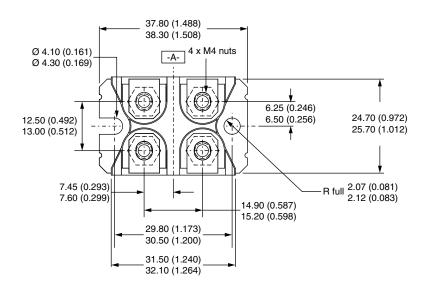
- Vishay Semiconductors product
- 3 4 5 HEXFRED® family
- Process designator (A = electron irradiated)
- Average current (140 = 140 A)
- Circuit configuration (two separate diodes, parallel pin-out)
- Package indicator (SOT-227 standard insulated base)
- Voltage rating (60 = 600 V)

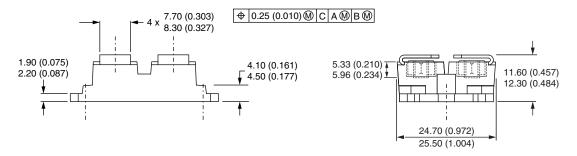
CIRCUIT CONFIGURATION					
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment 4			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Part marking information	www.vishay.com/doc?95425				

SOT-227 Generation 2

DIMENSIONS in millimeters (inches)





Note

· Controlling dimension: millimeter



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