

Product Termination Notification

Product Group: SIL/Wed Mar 1, 2023/PTN-SIL-011-2023-REV-0



Conversion to Copper (Cu) Wire - SQS405EN-T1_GE3

For further information, please contact your regional Vishay office.

CONTACT INFORMATION

Americas	Europe	Asia
Vishay Siliconix	Vishay Electronic GmbH	Vishay Intertechnology Asia Pte. Ltd
2565 Junction Ave	DrFelix-Zandman-Platz 1	37A Tampines Street 92 #07-01
	-	-
San Jose CA United States 95134	Selb Germany 95100	Singapore Singapore 528886
Phone: 4089705799	Phone: 49-9287-71 0	Phone: 65 6788 6668
Fax: 4089705799	Fax: 49-9287-70435	Fax: 65 6788 0988
business-americas@vishay.com	business-europe@vishay.com	business-asia@vishay.com

Description of Change: The affected part number listed in this notification will be converted to a Copper wire material set. The SQS405CENW-T1_GE3 replaces the SQS405EN-T1_GE3 and offers a long-term solution that is compatible with our latest package assembly methods. This silicon die uses exactly the same technology, process and "in-house" wafer fabrication facility as the existing parts. Any minor specification changes, (documented in the comparison sheets provided), reflect small lot to lot variations and improvements that have occurred over time since the original parts were released more than ten years ago. No final test limits have been changed apart from the avalanche test. In this case we have adopted new consistency rules whereby we test at or below the maximum rated current for the device which is -16A. Previously this test was performed at -19A in violation of the maximum rated current.

Classification of Change: Standardization of materials

Expected Influence on Quality/Reliability/Performance: None

Part Numbers/Series/Families Affected: SQS405EN-T1_GE3

Vishay Brand(S): Vishay Siliconix

Time Schedule:

Last Time Buy Date: Mon Sep 4, 2023 Last Time Ship Date: Mon Mar 4, 2024

Sample Availability: Qualified samples of replacement product are available immediately

Product Identification: SQS405CENW-T1_GE3

Qualification Data: AEC Q101 qualification data of replacement product is available. Qualification PPAP is available now

This PTN is considered approved, without further notification, unless we receive specific customer concerns before Fri Sep 1, 2023 or as specified by contract.

Issued By: Lance Gurrola, business-americas@vishay.com

Affected or Number ARC (2010 Gualified Vest 127 of Ve	None None None None None None None None
Package Type Process Technology Short National South Release Short National National Release Short National National Release Short National National Release Short National National National Release Short National Na	None None None None None None None None
Process Technology Solid cells of	None None None None None None None None
Bondwise Material 100% Rg & US Tested Ves Database Rev R	None None None None None None None None
DORS Age AUS Tested Ves Dotasheet Rey D	None None None None None None None None
Absolute Maxim Ratings	None None None None None None None None
Absolute Maksium Ratings	None None None None None None None None
Absolute Nations Symbol Test Conditions Limit Vision Symbol Limit Symbol Limit Symbol Limit Symbol Limit Symbol Symbol Limit Symbol Symbol Limit Symbol	None None None None None None None None
Dain Source Voltage	None None None None None None None None
Centinuous Dain Current	None None None None None None None None
Continuous Date Current (Code Conduction) S 1-5 25°C -15 A	None None None None None None None None
Section Source Current (Closed Conduction) S	None None None None None None None None
Public Polic Current	None None None None None None None None
Sign Public Additional Current AS L = 0.1ml -19 A A A A A A A A A	None None None None None None
Sight Pulse Assistance Foregry EAS	None None None None None
Max Power Dissipation	None None None None
Max Power Dissipation PO	None None None
Destring Aurolan and Strange temperature range T1	None None
Thermal Resistance A Republic Republ	None
Test Conditions Test Condi	
SecFicial processions Feet Conditions	
Test Conditions Ti-25'C urless otherwise noticed Test Conditions Min TYP MAX Units	
Type of t Type	
Gate Source Threshold Voltage VGS(th) VDS=VDS, D=250.A.	nange Risk
Signature Sign	None
VSS-V VSS-1V VSS-1V VSS-1V VSS-V VSS-1V VSS-V	None
2ero Vollage Onin Current	None
VSS-VIV VSS-12V, VSS-SV	None
Do-State Drain Current D(DN) ViSis-4.5V VDS-6V × 20 A D(DN) ViSis-4.5V VDS-6V × 20 A D(DN) ViSis-4.5V VDS-5V × 20 A D(DN) ViSis-4.5V DI-31.3A D(DN) D(DN) VISIS-4.5V DI-31.3A D(DN)	None
VSS-45V D-33.54 D-33	None
Dain Source On State Resistance Doin Visis-4-5V ID-313-8, Tp:125°C O.024 O.025 O	None
Dails Surger On-State Resistance ROS(m) VISS-45 V Di-13.54, Tj-175°C Di-22.6 D VISS-45 V Di-13.54, Tj-175°C Di-22.6 D VISS-45 V Di-13.54, Tj-175°C Di-12.6 D Di-12	None
ViSS-2 SV ID-12A 0.017 0.026 O ViSS-2 SV ID-12A 0.0166 0.0220 O Improvement O O O O O O O O O	None None
Forward Transconductance gls	None None
Injust Capacitance	
Cost	Toward .
Reverse Transfer Capacitance Crss 650 800 Crss 630 850 shipest test limits	NOTE
	variations. No
Total Gate Charge 49.8 75 Qg 53.6 81 changed.	None
Gate-Source Charge Qgs VGS=8V VDS=6V, ID=-10A 3.8 5.9 nC Qgs VGS=8V VDS=6V, ID=-4A 4 nC	None
Gate-Drain Charge Ogd 8.2 15 Ogd 9.6	None
Test limit re-cents	ed based on
Gate Resistance Rg f=1mHz 1.1 2.37 4 0 Rg f=1mHz 1.55 3.1 4.65 0 latest charcefizat	on methds. No
changes to the sil	on design and
process	
Turn-On Delay Time td(on) 27 34.5 td(on) 11 20 No actual change	$\overline{}$
Rise Time UVD0=6V, RL=0.60, ID=-1.5A, Vgen=-4.5V, Rg=10 29 35 ns UV VDD=6V, RL=1.5Q, ID=-4A, Vgen=-8V, Rg=10 6 12 ns only test condition	o the device None
Turn-Off Delay Time (td(off)) 59 72 (td(off) 58 90 (standardization	o the device None None
Fall Time tf 26 32 tf 26 40	None None
Pulsed Source-Drain Current ISM -64 A ISM -64 A None	None
Forward Voltage VSD If=10A VGS=0V -0.8 -1.1 V VSD I,=10A VGS=0V -0.81 -1.1 V None	None None
Body diode reverse recovery time trr ns trr 44 88 ns Additional chracte	None None None
Park finds were secured there	None None None None None None None None
0.00 10.00	None None None None None ization data mented None
	None None None None None None None None
Reverse recovery rise time th ns th 22 ns	None None None None None None None None
Body diode peak reverse recovery current houses 1.2 A super.	None None None None None None None None