

<b>PCN Number:</b>	20200128006	<b>PCN Date:</b>	Jan. 31, 2020
<b>Title:</b>	Datasheet for TAS5760M-Q1		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



TAS5760M-Q1

SLOS988B – SEPTEMBER 2017 – REVISED NOVEMBER 2019

Changes from Revision A (July 2018) to Revision B	Page
• Added <i>Feature</i> : Temperature grade 1: –40°C to +125°C.....	1
• Changed Features list item, Audio Performance From: R <sub>LOAD</sub> = 8Ω To: R <sub>SPK</sub> = 8Ω .....	1
• Added Junction Temperature, T <sub>J</sub> to the Absolute Maximum Ratings .....	4
• Added HBM and CDM classification levels to the ESD Ratings .....	5
• Changed Note 2 From: JEDEC Standard 4 Layer Board To: JEDEC Standard 2 Layer Board in the <i>Thermal Information</i> . ..	5
• Deleted the 48-pin packaged from the <i>Thermal Information</i> .....	5
• Changed the <i>Stereo BTL Using Software Control</i> to remove the 48-Pin information.....	49
• Changed the <i>Stereo BTL Using Hardware Control</i> to remove the 48-Pin information.....	52
• Changed the <i>Mono PBTL Using Software Control</i> to remove the 48-Pin information.....	54
• Changed the <i>Mono PBTL Using Hardware Control</i> to remove the 48-Pin information.....	57
• Deleted the 48-Pin DCA package BTL and PBTL images from the <i>Layout Examples</i> .....	63

The datasheet number will be changing.

Device Family	Change From:	Change To:
TAS5760M-Q1	SLOS988A	SLOS988B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TAS5760M-Q1>

#### Reason for Change:

To accurately reflect device characteristics.

#### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

#### Changes to product identification resulting from this PCN:

None.

#### Product Affected:

TAS5760MTDAPQ1	TAS5760MTDAPRQ1		
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For questions regarding this notice, e-mails can be sent to the contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
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